

edge lab LIVE at electronica 2026



Call for Participation

Conference Committee

Benjamin Cabé, the Zephyr Project
Josef Holzmayr, the Yocto Project and
Mender.io/Northern.tech*
Shawn Hymel, Skal Risa
Kudzai Manditereza, HiveMQ
Iain Menzies-Runciman, edgebench.io
Marc Pous, Edge Impulse
Mike Rohmoser, Digi International
Marta Rybczynska, Ygreky
David Sandys, Digi-Key
Kate Stewart, Linux Foundation
Martin Unverdorben, Tria, an Avnet company

* Conference chair

Important Dates

CFP opening:

10/06/2026

CFP closing:

31/07/2026

Notification of acceptance:

14/08/2026

Event date:

12/11/2026

Event location:

Live from Trade Fair Center Messe
München, Munich, Germany

Language

All presentations will be in English.

Submission link

<https://pretalx.com/edge-lab-live-at-electronica-2026/cfp>



electronica.de/en/trade-fair/program/edge-lab

INTRODUCTION

edge lab LIVE! is organizing a technical conference for senior embedded, IoT and edge AI technologists during electronica 2026, November 10–13, 2026 in Trade Fair Center Messe München, Munich, Germany.

The edge lab LIVE! technical conference will focus on practical aspects of modern embedded systems, software and secure development. The three-day program emphasizes content professional engineering audiences can immediately apply to their everyday jobs bringing up, porting to and deploying applications on real hardware.

Event attendees will consist primarily of senior professionals developing commercial products in the following roles:

- Embedded software developers
- System architects
- Machine learning engineers
- Application engineers
- Engineering managers
- Technical leads
- Product and compliance teams working on connected products
- Open-source developers of projects used in embedded, IoT and edge AI projects

CONFERENCE THEMES

In 2026, edge lab LIVE!'s conference program themes include:

Open-Source Software & Open Standards Hardware in the Field (including Zephyr Project, Yocto Project, PICMG & SGeT specifications): As embedded and edge systems become more complex, organizations increasingly depend on standards to ensure

interoperability across hardware platforms, operating systems, software frameworks and technology supply chains. Open standards play a critical role in enabling innovation, simplifying integration, supporting regulatory compliance and improving long-term maintainability.

Open-source software and open hardware standards topics of interest include (but are not limited to):

1. Balancing interoperability and scalability in open hardware standards for heterogeneous edge computing environments
2. Practical tips and pitfalls of open-source software test infrastructure and tools
3. Designing for compliance to functional safety, security and other standards with open-source software and open standards hardware
4. Lessons learned from developing and deploying open-source and open standards in the field

Edge & Physical AI pitfalls & practical tips:

Edge and physical AI systems are moving from simulation to real-world deployment, and confronting engineers with resource constraints, network bottlenecks, model complexity tradeoffs, reliability issues and security concerns. Edge AI Day focuses on developing edge AI and physical AI with optimized hardware architectures and adaptable software frameworks that trim development cycles and streamline time to market in connected embedded systems.

Edge AI topics of interest include (but are not limited to):

1. Energy-efficient inference on edge devices
2. Integrating machine learning with sensing, actuation and mechanical systems for edge applications
3. Frameworks, tools and techniques for efficient cross-platform development, implementation and execution of local AI models
4. Real-world setbacks and successes deploying AI in edge products

Cyber Resilience Act (CRA) implementation

best practices: CRA Day is focused on practical aspects of the European Cyber Resilience Act (CRA) for embedded systems, connected products and electronics supply chains. The goal is to bring together developers, architects, maintainers, electronics suppliers, integrators, security engineers, compliance teams and open-source contributors to share real-world experience, practical implementation approaches, lessons learned and tooling demonstrations.

CRA Day topics of interest include (but are not limited to):

1. CRA implementation status, Single Reporting Platform (SRP) experiences, supplier requirements and readiness
2. Security features and tools including update systems, hardware security features and secure configuration
3. Vendor relations with open source, vendor patches and upstream submissions
4. Vulnerability reporting and vulnerability management for products

Zephyr Project & Yocto Project meetups:

The Linux Foundation's Zephyr Project and Yocto Project will host meetups at edge lab LIVE! on Friday, November 13th. For more information on how to get involved, visit the respective project pages or contact:

Susan Remmert, Project Marketing, Linux Foundation.

CALL FOR PARTICIPATION

This call for participation invites technical abstracts, papers and presentation submissions related to the above topics. Submissions should ideally accommodate one of the following formats:

15-minute lightning talk—Compact overviews of new development trends or paradigms influencing the way embedded, IoT, or edge AI products are developed and brought to market. Published demo or example code is highly encouraged.

25-minute technical deep dives—Condensed presentations that take a direct and concentrated approach to solving common or widespread embedded, IoT, or edge AI engineering challenges in today's product development lifecycles. Published demo or example code is highly encouraged. Q&A time included in allocation.

25-minute panel sessions—Multi-member (minimum 3x) industry panels exploring current roadblocks and paths for technology and supply chain partners to overcome them. Q&A time included in allocation.

Note—60-90-minute hands-on workshops are available as sponsored sessions. For more information, contact Brandon Lewis, edge lab LIVE! conference director at brandon@edgelablive.com.

The Program Committee is especially interested in talks grounded in engineering reality rather than high-level marketing or purely theoretical compliance discussions. As a result, the ideal speaker profiles for edge lab LIVE! conference sessions include:

- Embedded Linux and Firmware developers
- Product engineers
- Security engineers
- Tooling and infrastructure developers
- Open-source maintainers
- Compliance and process specialists
- Representatives of national or sectorial engineering or cybersecurity bodies.

All proposals must be entered into the pretalx event management system at <https://pretalx.com/edge-lab-live-at-electronica-2026/cfp>. Submissions will be peer reviewed by the edge lab LIVE! Program Committee. Partial submissions will not be considered. A CFP review checklist is listed at the end of this document.

CONTACT

For questions regarding the CFP, please contact the edge lab LIVE! @ electronica 2026 Program Committee, edge lab LIVE! at electronica 2026 at cfp@edgelablive.com.



<https://pretalx.com/edge-lab-live-at-electronica-2026/cfp>

CFP REVIEW CHECKLIST

This review checklist is to be used by the Program Committee as a common ground framework for selecting a successful proposal.

- **Technical Relevance**

- The proposal is clearly related to CRA, embedded systems, connected products, electronics, or security engineering
- The proposal contains meaningful technical content
- The proposal is adapted to an embedded or industrial audience

- **Top Quality**

- The subject is clearly explained
- The proposal describes a concrete problem, experience, or solution
- The expected takeaways for attendees are understandable
- The technical depth matches the proposed format and duration

- **CFP Sub-Category**

- The proposal fits at least one CFP topic category
- The selected category is appropriate for the actual content

- **Practical Value**

- The proposal includes practical experience, return of experience, or lessons learned
- The proposal includes demonstrations, tooling, workflows, or implementation details
- The proposal avoids purely theoretical or regulatory-only discussions

- **Originality and Diversity**

- The proposal brings a new perspective, experience, or technical approach
- The proposal is not a duplicate of an existing talk (exceptions possible)

- **Ecosystem Perspective**

- The proposal considers interactions between suppliers, integrators, open-source projects, or product manufacturers
- Dependencies and supply-chain aspects are properly addressed when relevant

- **Speaker Experience**

- The speaker has relevant technical or operational experience on the subject
- Previous talks, publications, open-source work, or field experience support the proposal quality
- New speakers are welcome if the proposal quality is sufficient

- **Presentation Quality**

- The abstract is understandable and sufficiently detailed
- The structure and objectives of the presentation are clear
- The proposal appears feasible within the requested format

- **Exclusion Criteria**

- The proposal is not primarily marketing or product promotion
- The proposal is not excessively vague or unclear
- The proposal does not consist only of high-level compliance claims without technical substance
- The proposal does not duplicate an already accepted session too closely

- **Final Recommendation**

- Accept
- Weak Accept
- Waitlist
- Reject